

CLAIMS

What is claimed is:

- 5 1. A display driving system capable of reducing the required memory space, and simplifying the control circuit, the system comprising:
a plurality of memories capable of synchronously being read and written for being used as buffers for frame
10 accessing;
an image signal generator for processing the digital image;
a timing controller for controlling the plurality of memories capable of synchronously being read and written and the timing; and
15 a plurality of data drivers positioned on a display device panel for receiving the image data and displaying it on the panel.
- 20 2. The display driving system of claim 1, wherein the plurality of memories capable of synchronously being read and written are used for separately accessing the image data on the upper and lower half portions of the panel.
- 25 3. The display driving system of claim 1, wherein the output amount of the image data by the plurality of memories capable of synchronously being read and written is one

half of the input amount of the image data.

4. A display driving system capable of reducing the required memory space and simplifying the control circuit, comprising:
- a first memory for being a memory capable of synchronously being read and written;
 - a second memory for being a memory capable of synchronously being read and written;
 - an image signal generator for processing the digital image and being an image data source;
 - a timing controller for controlling the plurality of memories capable of synchronously being read and written and the timing;
 - a first data driver positioned on a display device panel for receiving the image data; and
 - a second data driver positioned on the display device panel for receiving the image data.
5. The display driving system of claim 4, wherein the first memory is connected to the first data driver.
6. The display driving system of claim 4, wherein the first memory is used for accessing the image data of the upper

half portion of the panel.

7. The display driving system of claim 4, wherein the second memory is connected to the second data driver.

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8. The display driving system of claim 4, wherein the second memory is used for accessing the image data of the lower half portion of the panel.

- 10 9. The display driving system of claim 1, wherein the output amount of the image data by the first memory and the second memory is a half of the input amount of the image data.

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